# An Efficient Residue to Binary Converter for the New Two-Level Moduli Set 

 $\left\{2^{2 n}\left\{2^{n}, 2^{n+1}-1\right\}, 2^{n}-1,2^{n}+1\right\}$Safi Seyyed Mohammad ${ }^{1}$, Rashno Meysam ${ }^{1}$, Abedi Parvin ${ }^{2}$, Kaboli Mina ${ }^{1}$ and Safi Fatemeh Sadat ${ }^{1}$<br>${ }^{1}$ Department of Computer Engineering, Ahvaz branch, Islamic Azad University, Ahvaz, IRAN<br>${ }^{2}$ Department of Computer Engineering, Ahvaz branch, Islamic Azad University, Shoushtar, IRAN

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#### Abstract

In this paper a new two-level four moduli set $\left\{2^{2 n}\left\{2^{n}, 2^{n+1}-1\right\}, 2^{n}-1,2^{n}+1\right\}$ is introduced and an efficient residue to binary converter is proposed for it. This moduli set contains the moduli set $\left\{2^{2 n}, 2^{n}-1,2^{n}+1\right\}$ in its first-level and the moduli set $\left\{2^{n}\right.$, $\left.2^{n+1}-1\right\}$ in its second-level for the modulo $2^{2 n}$. The reverse converter for this moduli set is implemented in two-level structure, which is designed based on Chinese remainder theorem (CRT) and the new CRT-1 methods. The proposed residue to binary converter for this moduli set improves the hardware cost and delay significantly in comparison to the similar previously presented moduli sets.


Keywords: Reverse converter, residue arithmetic, VLSI architecture.

## Introduction

The residue number system (RNS) is a carry-free number system, which can be used as a method for high-speed and lowpower implementation of digital signal processing (DSP) computation algorithms ${ }^{1}$. The residue to binary conversion is very important and complex part of an RNS system. The complexity of the residue to binary converter is mainly based on moduli set. Up to now, many moduli sets have been presented with various dynamic ranges (DR) such as $\left\{2^{\mathrm{n}+1}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}-1\right\}^{2}$, $\left\{2^{2 \mathrm{n}}, 2^{\mathrm{n}}-1,2^{\mathrm{n}+1}-1\right\}^{3}$ and $\left\{2^{\mathrm{n}}, 2^{2 \mathrm{n}}-1,2^{2 \mathrm{n}}+1\right\}^{4}$, which have dynamic ranges equal to $3 \mathrm{n}, 4 \mathrm{n}$ and 5 n -bits respectively. Some applications require large dynamic ranges with high parallelism. Therefore, four-moduli sets $\left\{2^{n}-1,2^{n}, 2^{n}+1,2^{n+1}-1\right\}^{5}$ and $\left\{2^{n}-\right.$ $\left.3,2^{n}-1,2^{n}+1,2^{n}+3\right\}^{6}$ have been presented. Since moduli set $\left\{2^{n}\right.$ $\left.-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1,2^{\mathrm{n}+1}-1\right\}$ has appropriate moduli, it has a more efficient RNS arithmetic unit compared to moduli set $\left\{2^{n}-3,2^{\text {n }}\right.$ $\left.-1,2^{n}+1,2^{n}+3\right\}$.

Hosseinzadeh et al have decreased the delay of reverse converter for moduli set $\left\{2^{n}-1,2^{n}, 2^{n}+1,2^{n+1}-1\right\}^{7}$. However, a little more hardware has been applied.

In this paper, an improved residue to binary converter is proposed for moduli set $\left\{2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1,2^{\mathrm{n}+1}-1\right\}$ by converting it into a two-level moduli set in the form of $\left\{2^{2 n}\left\{2^{n}, 2^{n+1}-1\right\}, 2^{n}\right.$ $\left.-1,2^{\mathrm{n}}+1\right\}$ such that its residue to binary converter has lower delay and hardware cost in comparison to the proposed converters for $\left\{2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1,2^{\mathrm{n}+1}-1\right\}^{5}$ and $\left\{2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1\right.$, $\left.2^{\mathrm{n}+1}-1\right\}^{7}$.

## Material and Methods

The $\operatorname{RNS}^{1}$ is based on a moduli set $\left\{m_{1}, m_{2}, \ldots, m_{n}\right\}$ which consists of pairwise relatively prime numbers. The dynamic range is defined as $M=m_{1} m_{2} \ldots m_{n}$. Each weighted number $X<$ $M$ has a unique representation in RNS as $\left(x_{1}, x_{2}, \ldots, x_{n}\right)$ where:
$x_{i}=X \bmod m_{i}=|X|_{m_{i}}, 0 \leq x_{i}<m_{i}$
By using CRT, the RNS number $\left(x_{1}, x_{2}, \ldots, x_{n}\right)$ can be converted into its equivalent weighted number as

$$
\begin{equation*}
X=\left|\sum_{i=1}^{n} \hat{m}_{i}\right| k_{i} \times\left.\left. x_{i}\right|_{m_{i}}\right|_{M} \tag{2}
\end{equation*}
$$

Where: $M=\prod_{i=1}^{n} m_{i}, \hat{m}_{i}=\frac{M}{m_{i}}, k_{i}=\hat{m}_{i}^{-1},\left|k_{i} \times \hat{m}_{i}\right|_{m_{i}}=1$
and $\quad x_{i}=|X|_{m_{i}}$.
By using CRT-1, the reverse conversion can be done as
$X=x_{1}+m_{1}\left|k_{1}\left(x_{2}-x_{1}\right)+k_{2} m_{2}\left(x_{3}-x_{2}\right)+\cdots+k_{n-1} m_{2} m_{3} \cdot \cdot m_{n-1}\left(x_{n}-x_{n-1}\right)\right|_{m_{2} m_{3} \cdot m_{n}}$
Where: $\left|k_{1} \times m_{1}\right|_{m_{2} m_{3} \cdots m_{n}}=1$,
$\left|k_{2} \times m_{1} \times m_{2}\right|_{m_{3} \cdots m_{n}}=1, \ldots,\left|k_{n-1} \times m_{1} \times m_{2} \times \cdots \times m_{n-1}\right|_{m_{n}}=1$.

In two-level RNS for each desired modulo at the first-level a moduli set at the second-level must be chosen in such a way that its dynamic range be equal or greater than the desired modulo at the first-level. In two-level RNS, arithmetic operations are performed on the residues of second-level moduli. Afterward,
for converting from RNS to binary system, the residues of the second-level are converted to corresponding residues at the firstlevel. Then, recently obtained residues are converted to binary system.

Residue to binary converter for the two-level moduli set $\left\{2^{2 n}\right.$ $\left.\left\{2^{n}, \mathbf{2}^{n+1}-1\right\}, \mathbf{2}^{n}-\mathbf{1}, \mathbf{2}^{n}+\mathbf{1}\right\}$ : the CRT-1 for these two moduli requires only one multiplicative inverse as

$$
\begin{equation*}
\left|k \times 2^{n}\right|_{2^{n+1}-1}=1 \rightarrow k=2 \tag{4}
\end{equation*}
$$

The $T=\left(x_{11}, x_{12}\right)$ can be obtained by substituting the value of $k$, and moduli $m_{11}=2^{n}, m_{12}=2^{n+1}$ in (3) as shown below

$$
\begin{align*}
& T=x_{11}+2^{n}\left|2\left(x_{12}-x_{11}\right)\right|_{2^{n+1}-1} \\
& =x_{11}+2^{n}\left|-2 x_{11}+2 x_{12}\right|_{2^{n+1}-1} \tag{5}
\end{align*}
$$

To calculate $x_{1}$ which is the corresponding residue of $m_{1}=2^{2 n}$, since the value of $x_{1}$ is in $0 \leq x_{1}<m_{1}$ span and the value of $T$ is in $0 \leq T<m_{11} \times m_{12}$ span and with respect to this reality that $m_{1}$ $\leq m_{11} \times m_{12}$, therefore below equation is used.

$$
x_{1}= \begin{cases}T & \text { if } \quad 0 \leq T<m_{1}  \tag{6}\\ T-m_{1} & \text { if } \quad m_{1} \leq T<m_{11} \times m_{12}\end{cases}
$$

The simplification of (5) can be performed with considering the point that, by expressing $x_{i}$ in $k$ bits, $\left|x_{i} \times 2^{p}\right|_{2^{k}-1}$ and $\left|-x_{i}\right|_{2^{k}-1}$ are equivalent to $p$ bits circular left shifting of $x_{i}$, and one's complement of $x_{i}$, respectively ${ }^{1}$. The residues can be represented at bit-level by: $x_{11}=\left(x_{11, n-1}, \ldots, x_{11,1}, x_{11,0}\right)$ and $x_{12}=\left(x_{12, \mathrm{n}}, \ldots\right.$, $x_{12,1}, x_{12,0}$ ). Therefore, (5) can be rewritten as

$$
\begin{align*}
& T=x_{11}+2^{n} H  \tag{7}\\
& H=\left|s_{1}+s_{2}\right|_{2^{n+1}-1}  \tag{8}\\
& s_{1}=\left|-2 x_{11}\right|_{2^{n+1}-1}=|-2(0 \underbrace{x_{11, n-1} \ldots x_{11,1} x_{11,0}}_{n})|_{2^{n+1}-1}=\underbrace{\bar{x}_{11, n-1} \ldots \bar{x}_{11,1} \bar{x}_{11,0}}_{n} 1  \tag{9}\\
& s_{2}=\left|2 x_{12}\right|_{2^{n+1}-1}=\mid 2(\left.\underbrace{x_{12, n} \ldots x_{12,1} x_{12,0}}_{n+1}\right|_{2^{n+1}-1}=\underbrace{x_{12, n-1} \ldots x_{12,1} x_{12,0} x_{12, n}}_{n+1} \tag{10}
\end{align*}
$$

By substituting (9) and (10) in (8), $H$ is obtained as a ( $n+1$ )-bits number. For calculating $T$, it is sufficient to concatenate $x_{11}$ to H.
$T=H_{n} \underbrace{H_{n-1} \ldots H_{1} H_{0} x_{11, n-1} \ldots x_{11,1} x_{11,0}}_{2 n}$
According to (11) equations (12) and (13) are concluded.

$$
\begin{equation*}
0 \leq T<m_{1} \quad \text { if } \quad H_{n}=0 \tag{12}
\end{equation*}
$$

$m_{1} \leq T<m_{11} \times m_{12}$ if $H_{n}=1$
With respect to (12) and (6) $x_{1}$ is obtained as follow
$x_{1}=\underbrace{0 H_{n-1} \ldots H_{1} H_{0} x_{11, n-1} \ldots x_{11,1} x_{11,0}}_{2 n}$
For the values greater than $m_{1}=2^{2 n}$ and based on (11) and (13), $T$ is equal to
$T=\underbrace{H_{n-1} \ldots H_{1} H_{0} x_{11, n-1} \ldots x_{11,1} x_{11,0}}_{2 n}$
The binary representation of $m_{1}=2^{2 n}$ can be shown as
$m_{1}=1 \underbrace{0000 \ldots 0000}_{2 n}$
By substituting (15) and (16) in (6), $x_{1}$ is obtained as
$x_{1}=\underbrace{0 H_{n-1} \ldots H_{1} H_{0} x_{11, n-1} \ldots x_{11,1} x_{11,0}}_{2 n}$
Since $x_{1}$ has the same value for $0 \leq T<m_{1}$ and $m_{1} \leq T<m_{11} \times$ $m_{12}$, the most significant bit of $x_{1}$ can be ignored as shown below
$x_{1}=\underbrace{H_{n-1} \ldots H_{1} H_{0} x_{11, n-1} \ldots x_{11,1} x_{11,0}}_{2 n}$
By calculating $x_{1}$ and using residues $x_{2}$ and $x_{3}$, the residue to binary converter for the first-level moduli set $\left\{2^{2 n}, 2^{n}-1,2^{n}+1\right\}$ is designed.

Residue to Binary converter for the moduli set $\left\{2^{2 n}, 2^{n}-1,2^{n}\right.$ $+\mathbf{1}\}$ based on CRT: according to (2) and by assuming $\mathrm{m}_{1}=2^{2 \mathrm{n}}$, $\mathrm{m}_{2}=2^{\mathrm{n}}-1$ and $\mathrm{m}_{3}=2^{\mathrm{n}}+1$ we have
$\hat{m}_{1}=\left(2^{2 n}-1\right), \hat{m}_{2}=2^{2 n}\left(2^{n}+1\right), \hat{m}_{3}=2^{2 n}\left(2^{n}-1\right)$ and
$M=2^{2 n}\left(2^{2 n}-1\right)$
Considering (19) the required multiplication reverses for (2) are computed as follows
$\left|k_{1} \times\left(2^{2 n}-1\right)\right|_{2^{2 n}}=1 \rightarrow k_{1}=-1$
$\left|k_{2} \times 2^{2 n}\left(2^{n}+1\right)\right|_{2^{n}-1}=1 \rightarrow k_{2}=2^{n-1}$
$\left|k_{3} \times 2^{2 n}\left(2^{n}-1\right)\right|_{2^{n}+1}=1 \rightarrow k_{3}=2^{n-1}$

The binary vectors $x_{1}, x_{2}$ and $x_{3}$ can be represented in bit-level as $\mathrm{x}_{1}=\left(\mathrm{H}_{\mathrm{n}-1}, \ldots, \mathrm{H}_{1}, \mathrm{H}_{0}, \mathrm{x}_{11, \mathrm{n}-1}, \ldots, \mathrm{x}_{11,1}, \mathrm{x}_{11}, 0\right)$, $\mathrm{x}_{2}=\left(\mathrm{x}_{2, \mathrm{n}-1}, \ldots, \mathrm{x}_{2}, 1, \mathrm{x}_{2,0}\right)$ and $\mathrm{x}_{3}=\left(\mathrm{x}_{3, \mathrm{n}}, \ldots, \mathrm{x}_{3,1}, \mathrm{x}_{3,0}\right)$. Now, (2) can be rewritten as

$$
\begin{equation*}
X=\left|\sum_{i=1}^{n} \hat{m}_{i}\right| k_{i} \times\left.\left. x_{i}\right|_{m_{i}}\right|_{M}=\sum_{i=1}^{n} \hat{m}_{i}\left|k_{i}\right|_{m_{i}} \times x_{i}-M \times l \tag{23}
\end{equation*}
$$

Where: $l$ is an integer number and depends on the value of X . By replacing (25)-(22) in (23) we have
$X=\left(\begin{array}{l}\left(2^{2 n}-1\right) \times(-1) \times x_{1}+ \\ 2^{2 n} \times\left(2^{n}+1\right) \times 2^{n-1} \times x_{2}+ \\ 2^{2 n} \times\left(2^{n}-1\right) \times 2^{n-1} \times x_{3}\end{array}\right)-2^{2 n} \times\left(2^{2 n}-1\right) \times l$
By dividing both sides of (24) by $2^{2 n}$ we have
$\frac{X}{2^{2^{n}}}=\left(\left(-1+2^{-2 n}\right) \times x_{1}+\left(2^{n}+1\right) \times 2^{n-1} \times x_{2}+\left(2^{n}-1\right) \times 2^{n-1} \times x_{3}\right)-\left(2^{2^{n}}-1\right) \times l$
and calculating the floor values in modulo $\left(2^{2 n}-1\right)$ results in the following
$\left\lfloor\frac{X}{2^{2 n}}\right\rfloor=\left|\begin{array}{l}-1 \times\left. x_{1}\right|_{\left(2^{2 n}-1\right)}+\left|\left(2^{n}+1\right) \times 2^{n-1} \times x_{2}\right|_{\left(2^{2 n}-1\right)} \\ +\left|\left(2^{n}-1\right) \times 2^{n-1} \times x_{3}\right|_{\left(2^{2 n}-1\right)}\end{array}\right|_{\left(2^{2 n}-1\right)}$
In this case, the number X can be computed by the following
$X=\left\lfloor\frac{X}{2^{2 n}}\right\rfloor \times 2^{2 n}+x_{1}$
Eq. (26) can be rewritten as
$\left\lfloor\frac{X}{2^{2 n}}\right\rfloor=\left|S_{3}+S_{4}+S_{51}+S_{52}\right|_{\left(2^{2 n}-1\right)}$
$S_{3}=\left|-x_{1}\right|_{\left(2^{2 n-1)}\right.}=|-(\underbrace{H_{n-1} \ldots H_{1} H_{0} x_{11, n-1} \ldots x_{11,1} x_{11,0}}_{2 n})|_{2^{2 n-1}}=$
$\underbrace{\bar{H}_{n-1} \ldots \bar{H}_{1} \bar{H}_{0} \bar{x}_{11, n-1} \ldots \bar{x}_{11,1} \bar{x}_{11,0}}_{2 n}$
$S_{4}=\left|\left(2^{2 n-1}+2^{n-1}\right) \times x_{2}\right|_{2^{2 n}-1}=|\left(2^{2 n-1}+2^{n-1}\right) \times(\underbrace{00 \ldots 00}_{n} \underbrace{x_{2, n-1} \ldots x_{2,1} x_{2,0}}_{n})|_{2^{2 n}-1}=$
$\underbrace{x_{2,0} x_{2, n-1} \ldots x_{2,1} x_{2,0}}_{n+1} \underbrace{x_{2, n-1} \ldots x_{2,1}}_{n-1}$
$S_{51}=\left|2^{2 n-1} \times x_{3}\right|_{2^{2 n}-1}=|2^{2 n-1}(\underbrace{00 \ldots 00}_{n-1} \underbrace{x_{3, n} \ldots x_{3,1} x_{3,0}}_{n+1})|_{2^{2 n}-1}$
$=x_{3,0} \underbrace{00 \ldots 00}_{n-1} \underbrace{x_{3, n} \ldots x_{3,1}}_{n}$
$S_{52}=\left|-2^{n-1} \times x_{3}\right|_{2^{2 n}-1}=|-2^{n-1}(\underbrace{00 \ldots 00}_{n-1} \underbrace{x_{3, n} \ldots x_{3,1} x_{3,0}}_{n+1})|_{2^{2 n}-1}$
$=\underbrace{\bar{x}_{3, n} \ldots \bar{x}_{3,1} \bar{x}_{3,0}}_{n+1} \underbrace{11 \ldots 11}_{n-1}$

The hardware implementation of residue to binary converter for the two-level moduli set $\left\{2^{2 n}\left\{2^{n}, 2^{n+1}-1\right\}, 2^{n}-1,2^{n}+1\right\}$ is illustrated in Figure-1. The required hardware consists of $n$ NOT gates in operand preparation unit 1 (opu1) which are used for calculating equation (9). To implement (8), a modulo ( $2^{n+1}-1$ ) adder is required. In this paper a ( $n+1$ )-bits carry propagate adder (CPA) with end around carry (EAC) is used to satisfy it. Opu2 contains ( $3 n+1$ ) NOT gates to calculate equations (29) and (32). Equation (28) is implemented by applying two $2 n$-bits carry-save adders (CSA) with EAC and one $2 n$-bits CPA with EAC. Some of the used full adders (FA) in CSA1 and CSA2 are reduced with pair of XOR/AND and XNOR/OR gates, because
equations (31) and (32) have some bits with constant values 0 or 1. Equation (27) is computed by concatenating $x_{1}$ with $\left\lfloor\frac{X}{2^{2 n}}\right\rfloor$ without any extra hardware.

## Results and Discussions

In Table- 1 the performance of the proposed residue to binary converter for the moduli set $\left\{2^{2 \mathrm{n}}\left\{2^{\mathrm{n}}, 2^{\mathrm{n}+1}-1\right\}, 2^{\mathrm{n}}-1,2^{\mathrm{n}}+1\right\}$ has been compared with converters for $\left\{2^{n}-1,2^{n}, 2^{n}+1,2^{\text {n+1 }}-1\right\}^{5}$ and $\left\{2^{\mathrm{n}}-1,2^{\mathrm{n}}, 2^{\mathrm{n}}+1,2^{\mathrm{n}+1}-1\right\}^{7}$ from both hardware cost and delay viewpoints. As shown in figure-1, the delay of opu1 and opu2 are equal to one NOT gate and CSA 1 and CSA2 have the delay of one full adder. In addition, the delay of CPA1 and CPA2 is equal to $(2 n+2) t_{\mathrm{FA}}$ and $(4 n) t_{\mathrm{FA}}$ respectively, where $\mathrm{t}_{\mathrm{FA}}$ denotes the delay of a full adder (FA). For a better comparison, the unit gate model is considered to obtain total area and delay estimations. Based on this model, each two-input monotonic gate counts as one gate in area and delay, an XOR/XNOR gate counts as two gates in area and delay, and an FA has area of seven gates and delay of four gates. The corresponding total unit gate area and delay are presented in table-1. According to the results of table-1, our proposed residue to binary converter has significant reduction in both delay and hardware cost in comparison to the converters presented for $\left\{2^{n}-1,2^{n}, 2^{n}+1,2^{n+1}\right.$ $-1\}^{5}$ and $\left\{2^{n}-1,2^{n}, 2^{n}+1,2^{n+1}-1\right\}^{7}$ moduli sets.

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## Conclusion

This paper presents an efficient two-level design of reverse converter for the new two-level moduli set $\left\{2^{2 n}\left\{2^{n}, 2^{n+1}-1\right\}, 2^{n}\right.$ $\left.-1,2^{n}+1\right\}$ based on combination of CRT and New CRT-1. Comparison with the similar four-moduli residue to binary converters show that the proposed design is faster and requires less hardware area.

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(b)

Figure- 1
The proposed residue to binary converter: (a) second-level (b) first-level
Table-1
Performance Comparison

| Converter | Area | Unit Gate Area | Delay | Unit Gate Delay |
| :---: | :---: | :---: | :---: | :---: |
| [5]-CI | $\begin{aligned} & \left(9 \mathrm{n}+5+\mathrm{k}^{*}\right) \mathrm{A}_{\mathrm{FA}}+(2 \mathrm{n}) \mathrm{A}_{\mathrm{XNOR}} \\ & +(2 \mathrm{n}) \mathrm{A}_{\mathrm{OR}}+(6 \mathrm{n}+1) \mathrm{A}_{\mathrm{NOT}} \\ & \hline \end{aligned}$ | $\begin{aligned} & \left(129 n+7 n^{2}\right) / 2 \\ & +4 \end{aligned}$ | $((23 n+12) / 2) \mathrm{t}_{\mathrm{FA}}$ | $46 n$ |
| [7] | $\begin{aligned} & \left(10 \mathrm{n}+6+\mathrm{k}^{*}\right) \mathrm{A}_{\mathrm{FA}}+(6 \mathrm{n}+2) \mathrm{A}_{\mathrm{XNOR}} \\ & +(6 \mathrm{n}+2) \mathrm{A}_{\mathrm{OR}}+(7 \mathrm{n}+2) \mathrm{A}_{\mathrm{NOT}} \\ & +(\mathrm{n}+3) \mathrm{A}_{\mathrm{MUX} 21}+(2 \mathrm{n}+1) \mathrm{A}_{\mathrm{MUX} 3-1} \end{aligned}$ | $\begin{aligned} & \left(193 n+7 n^{2}\right) / 2 \\ & +50 \end{aligned}$ | $((15 n+22) / 2) \mathrm{t}_{\mathrm{FA}}$ | 30n |
| $\begin{aligned} & \text { D} \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | $\begin{aligned} & (5 \mathrm{n}+3) \mathrm{A}_{\mathrm{FA}}+(\mathrm{n}+1) \mathrm{A}_{\mathrm{XOR}} \\ & +(\mathrm{n}+1) \mathrm{A}_{\mathrm{AND}}+(\mathrm{n}+1) \mathrm{A}_{\mathrm{XNOR}} \\ & +(\mathrm{n}+1) \mathrm{A}_{\mathrm{OR}}+(4 \mathrm{n}+1) \mathrm{A}_{\mathrm{NOT}} \end{aligned}$ | $(47 n+30)$ | $(6 \mathrm{n}+4) \mathrm{t}_{\mathrm{FA}}+2 \mathrm{t}_{\mathrm{NOT}}$ | $24 n$ |

[^0]
[^0]:    * $\mathrm{k}=(\mathrm{n}-4)^{*}(\mathrm{n}+2) / 2$

